



11A, 650V SUPER JUNCTION MOS POWER TRANSISTOR

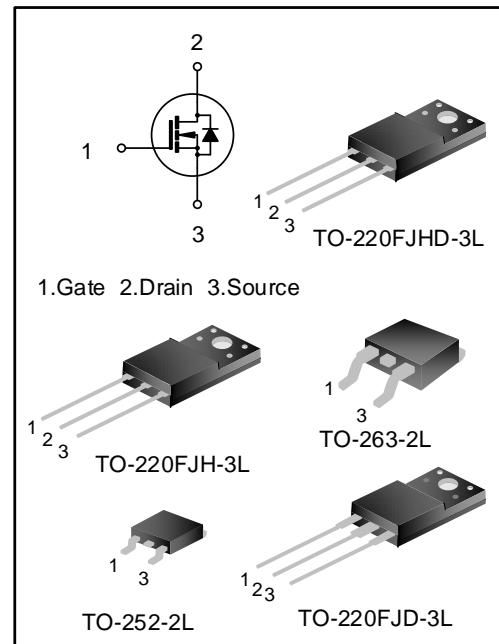
DESCRIPTION

SVSP65R380FJD(D)(FJH)(S)(FJHD)D4 is an N-channel enhancement mode high voltage power MOSFETs produced using Silan's super junction MOS technology. It achieves low conduction loss and switching losses. It leads the design engineers to their power converters with high efficiency, high power density, and superior thermal behavior.

Furthermore, it's universal applicable, i.e., suitable for hard and soft switching topologies.

FEATURES

- ◆ 11A, 650V, $R_{DS(on)(typ.)}=0.32\Omega @ V_{GS}=10V$
- ◆ New revolutionary high voltage technology
- ◆ Ultra low gate charge
- ◆ Periodic avalanche rated
- ◆ Extreme dv/dt rated
- ◆ High peak current capability
- ◆ 100% avalanche tested
- ◆ Pb-free lead plating
- ◆ RoHS compliant



KEY PERFORMANCE PARAMETERS

| Characteristics | Ratings | Unit |
|--------------------|---------|----------|
| $V_{DS}@T_{J,max}$ | 700 | V |
| $V_{GS(th)}$ | 2.5~4.5 | V |
| $R_{DS(on),max.}$ | 0.38 | Ω |
| $I_{D,pulse}$ | 44 | A |
| $Q_{g,typ.}$ | 20 | nC |

ORDERING INFORMATION

| Part No. | Package | Marking | Hazardous Substance Control | Packing Type |
|------------------|---------------|-----------|-----------------------------|--------------|
| SVSP65R380FJDD4 | TO-220FJD-3L | P65R380D4 | Halogen free | Tube |
| SVSP65R380DD4TR | TO-252-2L | P65380D4 | Halogen free | Tape & Reel |
| SVSP65R380FJHD4 | TO-220FJH-3L | P65R380D4 | Halogen free | Tube |
| SVSP65R380SD4TR | TO-263-2L | P65R380D4 | Halogen free | Tape & Reel |
| SVSP65R380SD4 | TO-263-2L | P65R380D4 | Halogen free | Tube |
| SVSP65R380FJHDD4 | TO-220FJHD-3L | P65R380D4 | Halogen free | Tube |

ABSOLUTE MAXIMUM RATINGS (UNLESS OTHERWISE NOTED, $T_J=25^\circ\text{C}$)

| Characteristics | Symbol | Test conditions | Ratings | | | Unit |
|--|---------------|---|---------|------|------|------------------|
| | | | Min. | Typ. | Max. | |
| Gate-source Voltage (Static) | V_{GS} | -- | -20 | -- | 20 | V |
| Gate-source Voltage (Dynamic) | V_{GS} | AC($f>1\text{Hz}$) | -30 | -- | 30 | V |
| Drain Current | I_D | $T_C=25^\circ\text{C}$ | -- | -- | 11 | A |
| | | $T_C=100^\circ\text{C}$ | -- | -- | 7.0 | A |
| Drain Current Pulsed (Note 1) | I_{DM} | $T_C=25^\circ\text{C}$ | -- | -- | 44 | A |
| Power Dissipation (TO-220FJD-3L) (TO-220FJH-3L) (TO-220FJHD-3L) (Note 2) | P_D | $T_C=25^\circ\text{C}$ | -- | -- | 23 | W |
| Power Dissipation (TO-252-2L) (Note 2) | P_D | $T_C=25^\circ\text{C}$ | -- | -- | 104 | W |
| Power Dissipation (TO-263-2L) (Note 2) | P_D | $T_C=25^\circ\text{C}$ | -- | -- | 117 | W |
| Single Pulsed Avalanche Energy | E_{AS} | $L=79\text{mH}$, $V_{DD}=100\text{V}$, $R_G=25\Omega$, starting temperature $T_J=25^\circ\text{C}$ | -- | -- | 219 | mJ |
| Single Pulsed Current | I_{AS} | -- | -- | -- | 2.2 | A |
| Reverse Diode dv/dt | dv/dt | $V_{DS}=0\sim400\text{V}$, $I_{SD}\leq I_S$, $T_J=25^\circ\text{C}$ | -- | -- | 15 | V/ns |
| MOSFET dv/dt Ruggedness | dv/dt | $V_{DS}=0\sim480\text{V}$ | -- | -- | 50 | V/ns |
| Operation Junction Temperature Range | T_J | -- | -55 | -- | 150 | $^\circ\text{C}$ |
| Storage Temperature Range | T_{stg} | -- | -55 | -- | 150 | $^\circ\text{C}$ |
| Continuous Diode Forward Current | I_S | $T_C=25^\circ\text{C}$, integral reverse P-N junction diode in the MOSFET | -- | -- | 11 | A |
| Diode Pulse Current | $I_{S,pulse}$ | | -- | -- | 44 | A |
| Maximum Diode Commutation Speed | di/dt | $V_{DS}=0\sim400\text{V}$, $I_{SD}\leq I_S$, $T_J=25^\circ\text{C}$ | -- | -- | 500 | A/ μs |



THERMAL CHARACTERISTICS

Table1. TO-220FJD-3L/TO-220FJH-3L/TO-220FJHD-3L (SVSP65R380FJD/FJH/FJHDD4)

| Characteristics | Symbol | Test conditions | Ratings | | | Unit |
|---|-------------------|---|---------|------|------|------|
| | | | Min. | Typ. | Max. | |
| Thermal Resistance, Junction-case, Bottom | R _{θJC} | -- | -- | -- | 5.4 | °C/W |
| Thermal Resistance, Junction-ambient | R _{θJA} | -- | -- | -- | 62.5 | °C/W |
| Soldering Temperature (in line) | T _{sold} | 15 ⁺² ₋₀ sec, 1time | -- | -- | 260 | °C |

Table 2. TO-252-2L (SVSP65R380DD4)

| Characteristics | Symbol | Test conditions | Ratings | | | Unit |
|---|-------------------|------------------------------------|---------|------|------|------|
| | | | Min. | Typ. | Max. | |
| Thermal Resistance, Junction-case, Bottom | R _{θJC} | -- | -- | -- | 1.2 | °C/W |
| Thermal Resistance, Junction-ambient | R _{θJA} | -- | -- | -- | 62.0 | °C/W |
| Soldering Temperature (SMD) | T _{sold} | Reflow soldering: 10±1 sec, 3times | -- | -- | 260 | °C |

Table 3. TO-263-2L (SVSP65R380SD4)

| Characteristics | Symbol | Test conditions | Ratings | | | Unit |
|---|-------------------|------------------------------------|---------|------|------|------|
| | | | Min. | Typ. | Max. | |
| Thermal Resistance, Junction-case, Bottom | R _{θJC} | -- | -- | -- | 1.07 | °C/W |
| Thermal Resistance, Junction-ambient | R _{θJA} | -- | -- | -- | 62.5 | °C/W |
| Soldering Temperature (SMD) | T _{sold} | Reflow soldering: 10±1 sec, 3times | -- | -- | 260 | °C |



ELECTRICAL CHARACTERISTICS (UNLESS OTHERWISE NOTED, $T_J=25^\circ\text{C}$)

Static characteristics

| Characteristics | Symbol | Test conditions | Ratings | | | Unit |
|---|----------------------------|---|---------|------|----------|---------------|
| | | | Min. | Typ. | Max. | |
| Drain-source Breakdown Voltage | BV_{DSS} | $V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$ | 650 | -- | -- | V |
| Drain-source Leakage Current | I_{DSS} | $V_{\text{DS}}=650\text{V}, V_{\text{GS}}=0\text{V}, T_J=25^\circ\text{C}$ | -- | -- | 1.0 | μA |
| | | $V_{\text{DS}}=650\text{V}, V_{\text{GS}}=0\text{V}, T_J=150^\circ\text{C}$ | -- | 2.5 | -- | |
| Gate-source Leakage Current | I_{GSS} | $V_{\text{GS}}=\pm20\text{V}, V_{\text{DS}}=0\text{V}$ | -- | -- | ±100 | nA |
| Gate Threshold Voltage | $V_{\text{GS}(\text{th})}$ | $V_{\text{GS}}=V_{\text{DS}}, I_{\text{D}}=250\mu\text{A}$ | 2.5 | -- | 4.5 | V |
| Static Drain-source On State Resistance | $R_{\text{DS}(\text{on})}$ | $V_{\text{GS}}=10\text{V}, I_{\text{D}}=5.5\text{A}, T_J=25^\circ\text{C}$ | -- | 0.32 | 0.38 | Ω |
| | | $V_{\text{GS}}=10\text{V}, I_{\text{D}}=5.5\text{A}, T_J=150^\circ\text{C}$ | -- | 0.72 | -- | |
| Gate Resistance | R_{G} | $f=1\text{MHz}$ | -- | 3.2 | -- | Ω |

Dynamic characteristics

| Characteristics | Symbol | Test conditions | Ratings | | | Unit |
|------------------------------|----------------------------|---|---------|------|------|-------------|
| | | | Min. | Typ. | Max. | |
| Input Capacitance | C_{iss} | $f=1\text{MHz}, V_{\text{GS}}=0\text{V}, V_{\text{DS}}=100\text{V}$ | -- | 678 | -- | pF |
| Output Capacitance | C_{oss} | | -- | 30 | -- | |
| Reverse Transfer Capacitance | C_{rss} | | -- | 0.9 | -- | |
| Turn-on Delay Time | $t_{\text{d}(\text{on})}$ | $V_{\text{DD}}=325\text{V}, V_{\text{GS}}=10\text{V}, R_{\text{G}}=24\Omega, I_{\text{D}}=11\text{A}$ (Notes 3, 4) | -- | 14 | -- | ns |
| Turn-on Rise Time | t_{r} | | -- | 36 | -- | |
| Turn-off Delay Time | $t_{\text{d}(\text{off})}$ | | -- | 48 | -- | |
| Turn-off Fall Time | t_{f} | | -- | 28 | -- | |
| Total Gate Charge | Q_{g} | $V_{\text{DD}}=520\text{V}, V_{\text{GS}}=10\text{V}, I_{\text{D}}=11\text{A}$ (Notes 3, 4) | -- | 20 | -- | nC |
| Gate-source Charge | Q_{gs} | | -- | 5.7 | -- | |
| Gate-drain Charge | Q_{gd} | | -- | 10 | -- | |
| Gate-plateau Voltage | V_{plateau} | | -- | 7.2 | -- | V |

Reverse diode characteristics

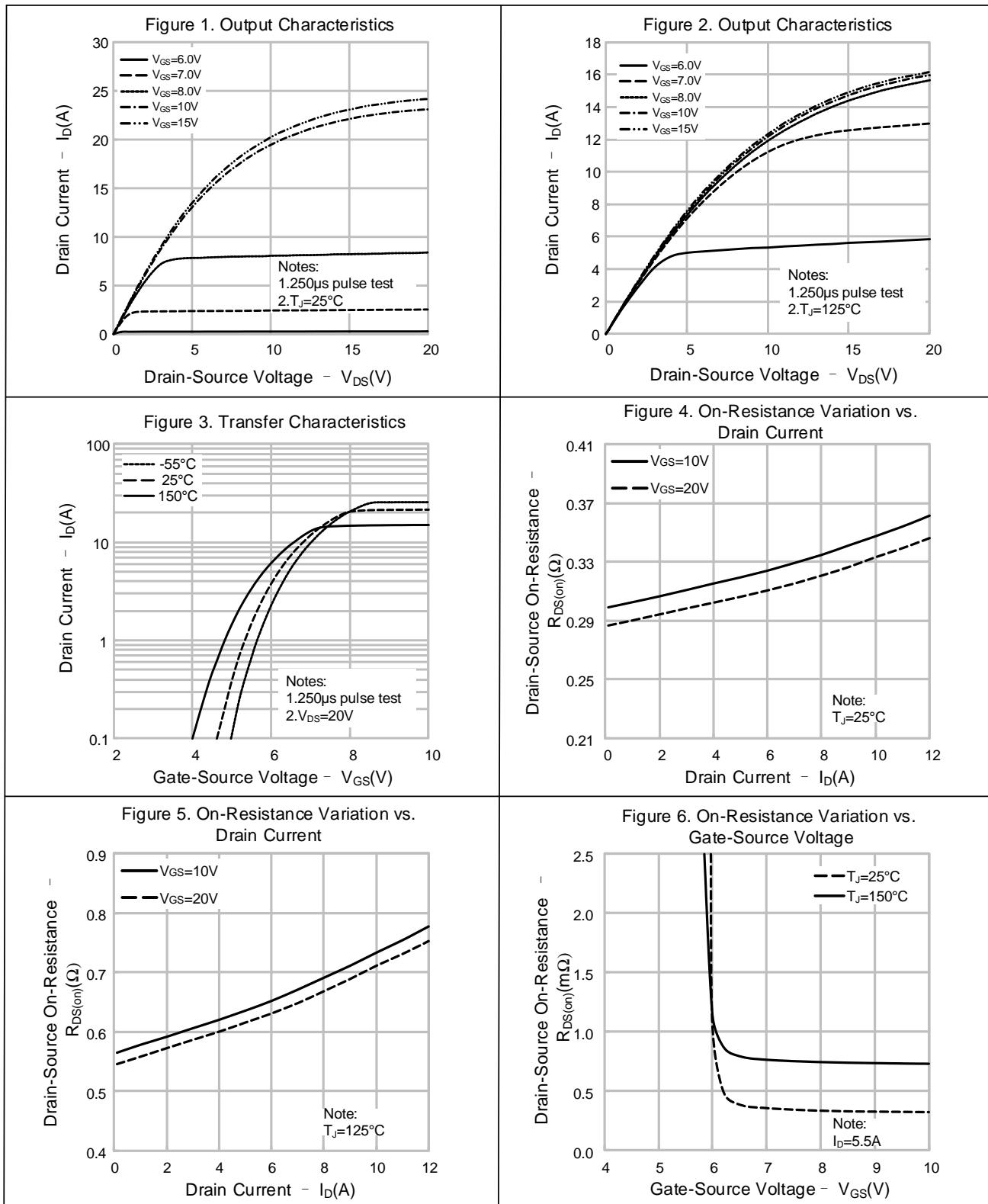
| Characteristics | Symbol | Test conditions | Ratings | | | Unit |
|-------------------------------|------------------|---|---------|------|------|-------------|
| | | | Min. | Typ. | Max. | |
| Diode Forward Voltage | V_{SD} | $I_{\text{S}}=11\text{A}, V_{\text{GS}}=0\text{V}$ | -- | -- | 1.4 | V |
| Reverse Recovery Time | T_{rr} | $I_{\text{S}}=11\text{A}, V_{\text{GS}}=0\text{V}, V_{\text{R}}=50\text{V}, \frac{dI_{\text{F}}}{dt}=100\text{A}/\mu\text{s}$ (Note 3) | -- | 276 | -- | ns |
| Reverse Recovery Charge | Q_{rr} | | -- | 3.34 | -- | |
| Reverse Recovery Peak Current | I_{rrm} | | -- | 24 | -- | A |

Notes:

1. Pulse time $5\mu\text{s}$;
2. The dissipation power will change with temperature, derating above 25°C :
 $0.19\text{W}/^\circ\text{C}(\text{TO-220FJD-3L}) (\text{TO-220FJH-3L}) (\text{TO-220FJHD-3L})/0.83\text{W}/^\circ\text{C}(\text{TO-252-2L})/0.93\text{W}/^\circ\text{C}(\text{TO-263-2L})$;
3. Pulse Test: Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$;
4. Essentially independent of operating temperature.



TYPICAL CHARACTERISTICS





TYPICAL CHARACTERISTICS (CONTINUED)

Figure 7. Gate-source Threshold Voltage vs. Temperature

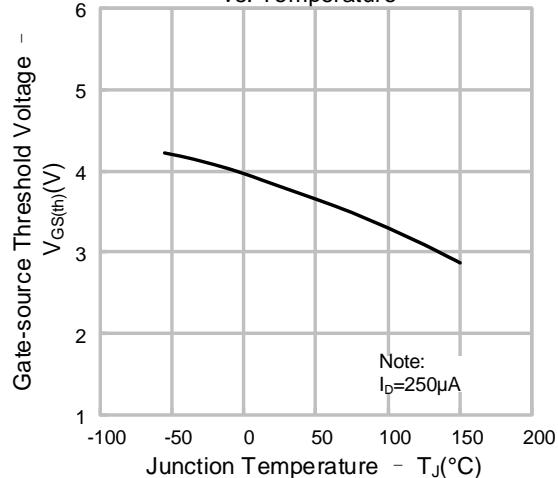


Figure 8. Body Diode Forward Voltage Variation vs. Source Current and Temperature

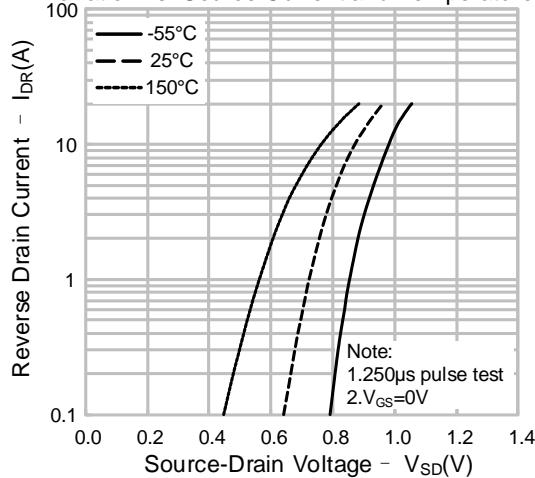


Figure 9. Capacitance Characteristics

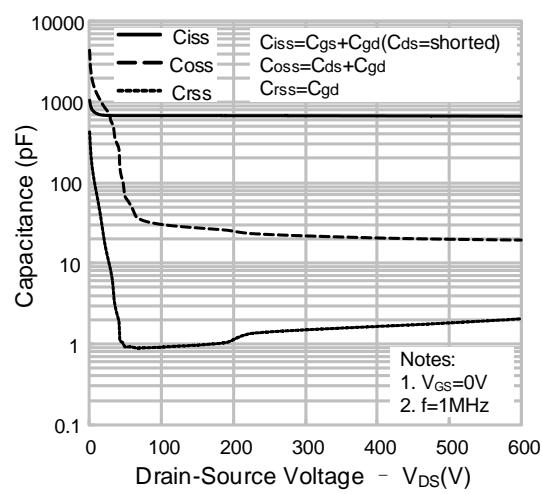


Figure 10. Gate Charge Characteristics

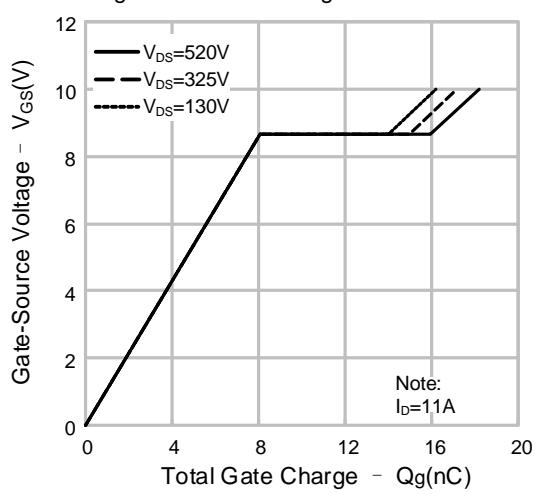


Figure 11. Breakdown Voltage Variation vs. Temperature

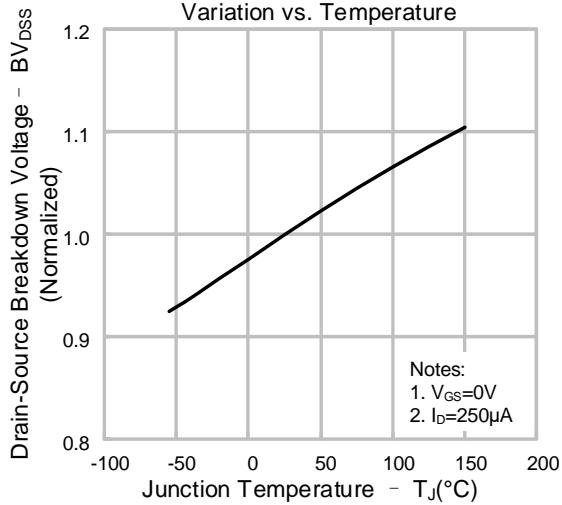
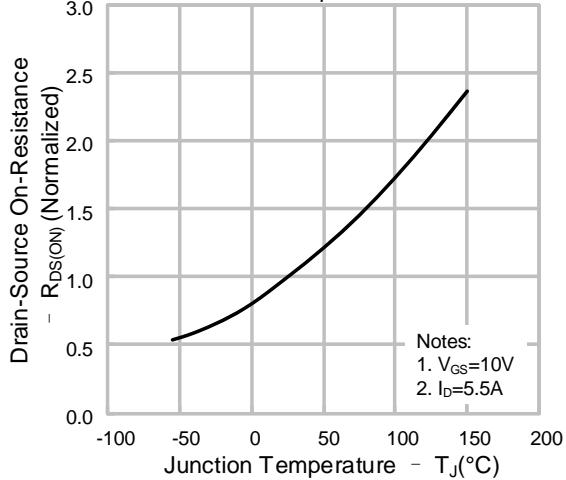
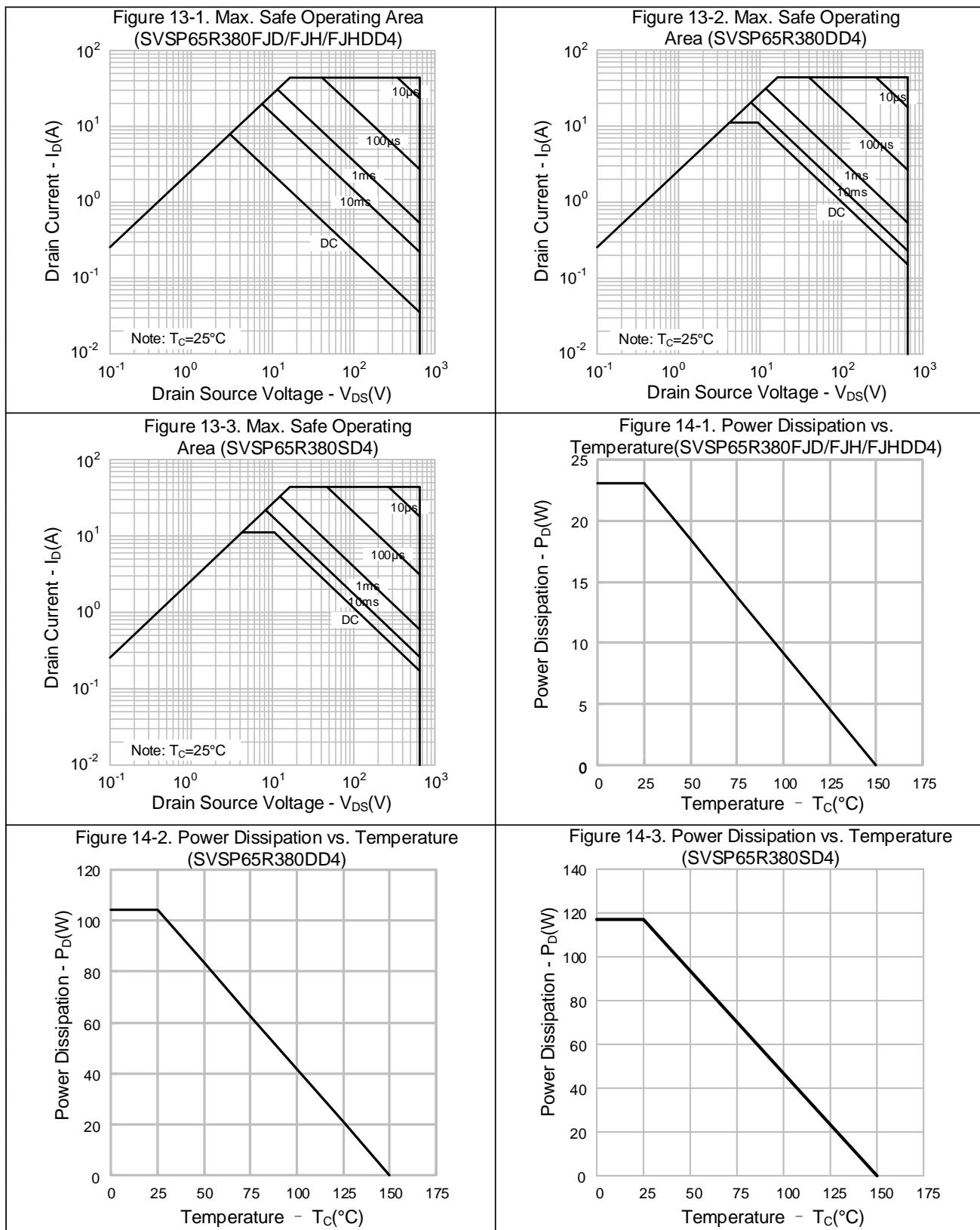


Figure 12. On-resistance Variation vs. Temperature





TYPICAL CHARACTERISTICS (CONTINUED)





TYPICAL CHARACTERISTICS (CONTINUED)

Figure 15. Drain Current vs. Case Temperature

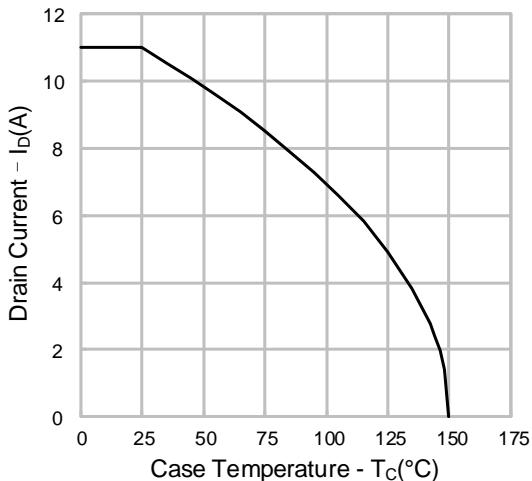


Figure 16-1. Transient Thermal Impedance vs. On-pulse Duration(SVSP65R380FJD/FJH/FJHDD4)

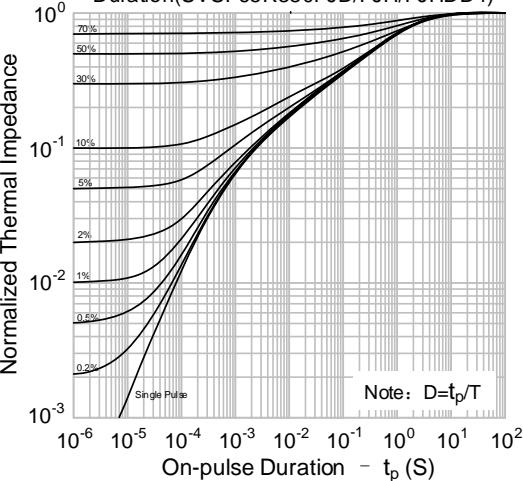


Figure 16-2. Transient Thermal Impedance vs. On-pulse Duration(SVSP65R380DD4)

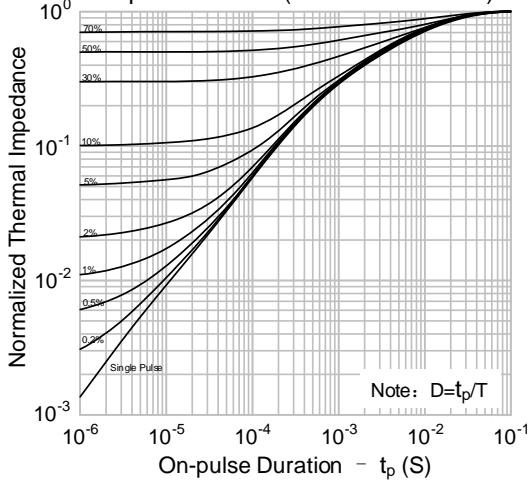
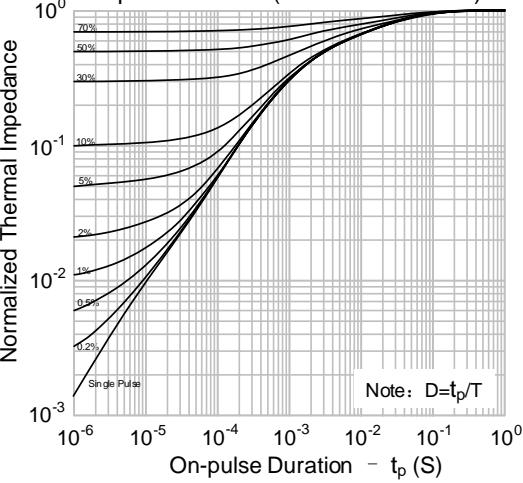


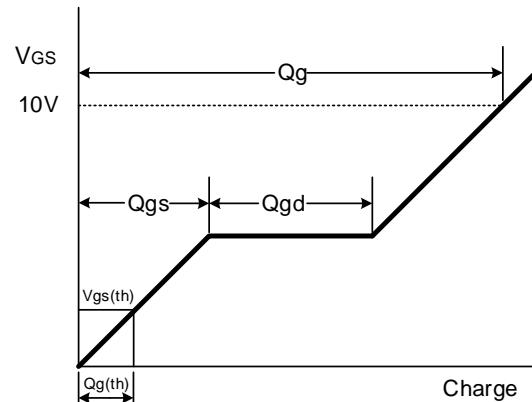
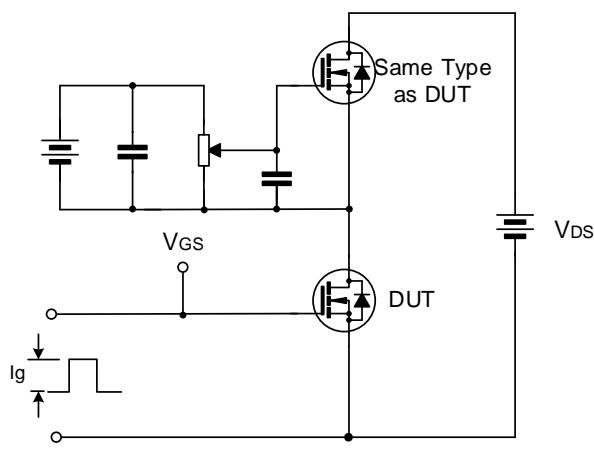
Figure 16-3. Transient Thermal Impedance vs. On-pulse Duration(SVSP65R380SD4)



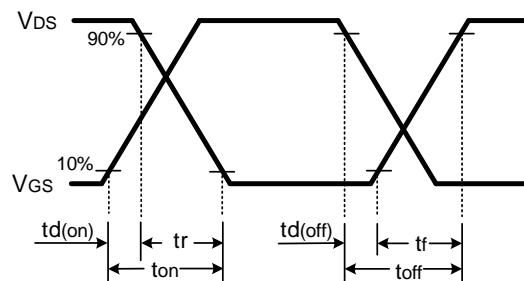
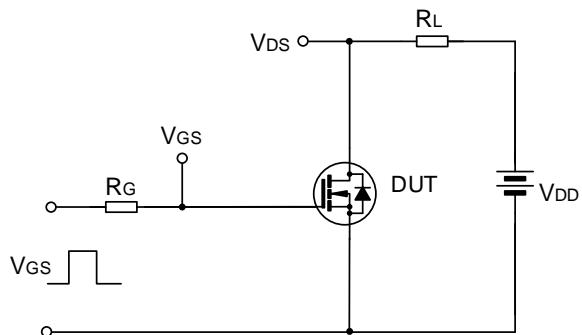


TYPICAL TEST CIRCUIT

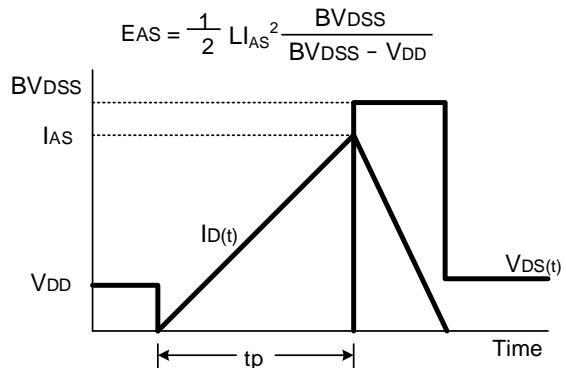
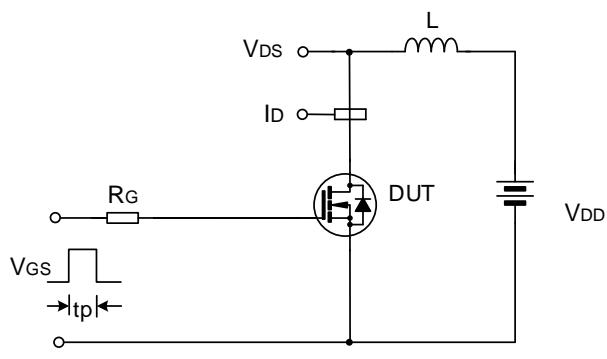
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveform



Unclamped Inductive Switching Test Circuit & Waveform

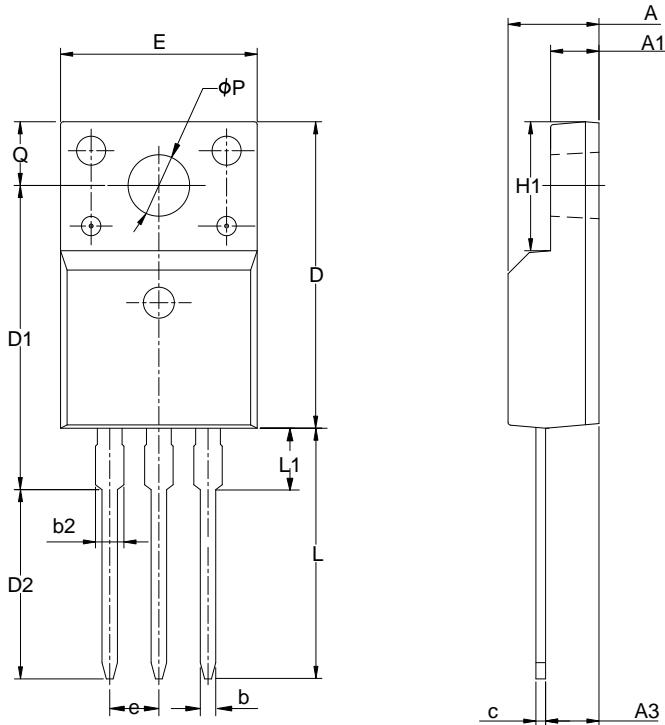




PACKAGE OUTLINE

TO-220FJD-3L

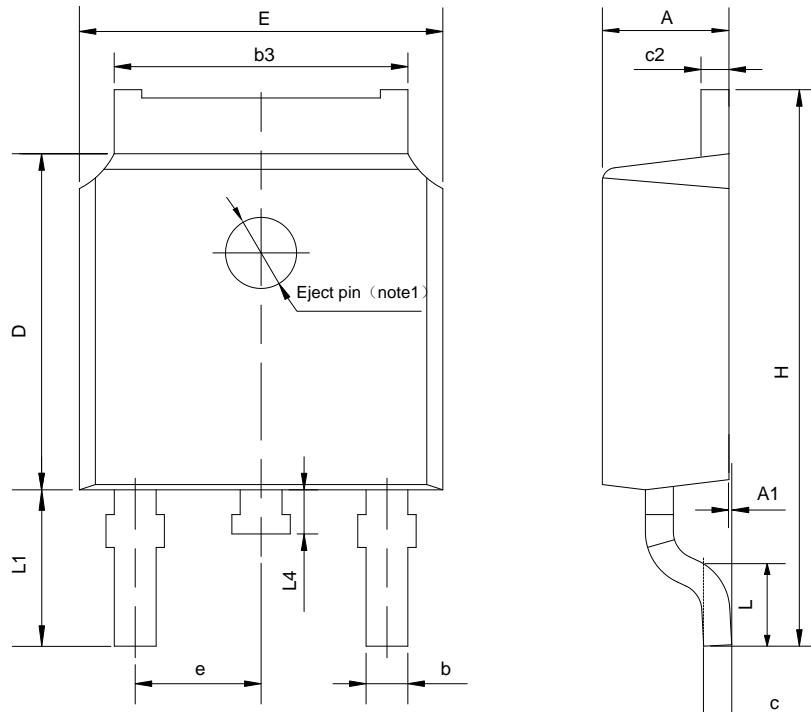
UNIT: mm



| SYMBOL | MILLIMETER | | |
|--------|------------|-------|-------|
| | MIN | NOM | MAX |
| A | 4.42 | 4.70 | 5.02 |
| A1 | 2.30 | 2.54 | 2.80 |
| A3 | 2.50 | 2.76 | 3.10 |
| b | 0.55 | 0.70 | 0.85 |
| b2 | — | — | 1.29 |
| c | 0.35 | 0.50 | 0.65 |
| D | 15.25 | 15.87 | 16.25 |
| D1 | 13.97 | 14.47 | 14.97 |
| D2 | 10.58 | 11.08 | 11.58 |
| E | 9.73 | 10.16 | 10.36 |
| e | 2.54BSC | | |
| H1 | 6.40 | 6.68 | 7.00 |
| L | 12.48 | 12.98 | 13.48 |
| L1 | — | — | 2.00 |
| φP | 3.00 | 3.18 | 3.40 |
| Q | 3.05 | 3.30 | 3.55 |

TO-252-2L

UNIT: mm



| SYMBOL | MILLIMETER | | |
|--------|------------|-------|-------|
| | MIN | NOM | MAX |
| A | 2.10 | 2.30 | 2.50 |
| A1 | 0 | — | 0.127 |
| b | 0.66 | 0.76 | 0.89 |
| b3 | 5.10 | 5.33 | 5.46 |
| c | 0.45 | — | 0.65 |
| c2 | 0.45 | — | 0.65 |
| D | 5.80 | 6.10 | 6.40 |
| E | 6.30 | 6.60 | 6.90 |
| e | 2.30TYP | | |
| H | 9.60 | 10.10 | 10.60 |
| L | 1.40 | 1.50 | 1.70 |
| L1 | 2.90REF | | |
| L4 | 0.60 | 0.80 | 1.00 |

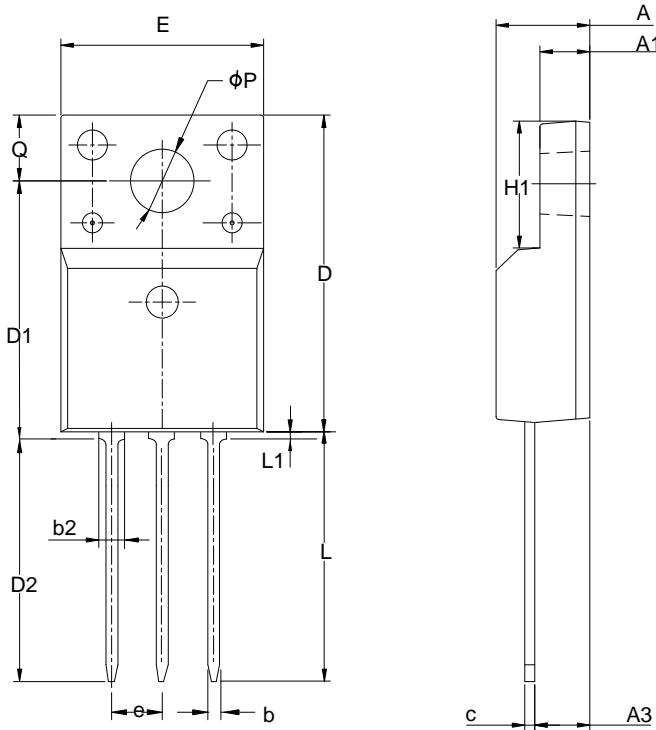
NOTE1 : There are two conditions for this position:has an eject pin or has no eject pin.



PACKAGE OUTLINE(CONTINUED)

TO-220FJH-3L

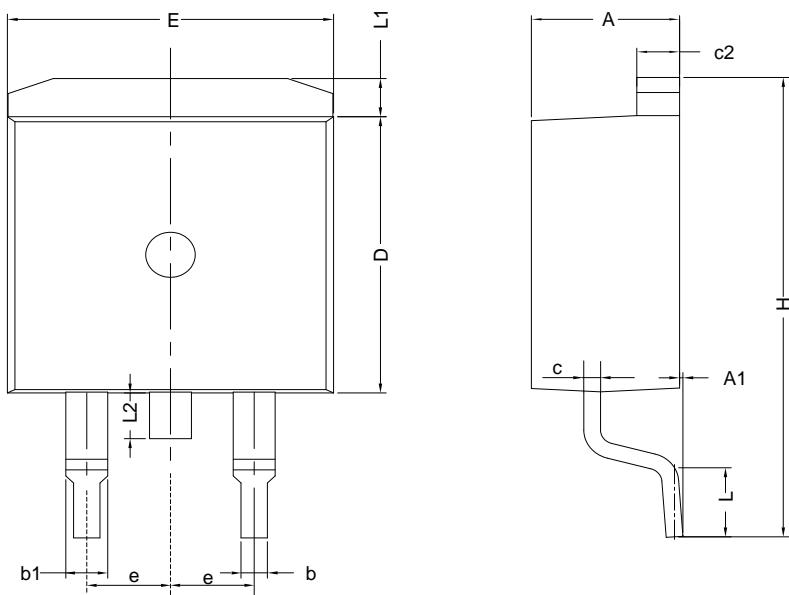
UNIT: mm



| SYMBOL | MILLIMETER | | |
|--------|------------|-------|-------|
| | MIN | NOM | MAX |
| A | 4.42 | 4.70 | 5.02 |
| A1 | 2.30 | 2.54 | 2.80 |
| A3 | 2.50 | 2.76 | 3.10 |
| b | 0.55 | 0.70 | 0.80 |
| b2 | — | — | 1.29 |
| c | 0.35 | 0.50 | 0.65 |
| D | 15.25 | 15.87 | 16.25 |
| D1 | 12.87 | 13.07 | 13.27 |
| D2 | 12.28 | 12.48 | 12.68 |
| E | 9.73 | 10.16 | 10.36 |
| e | 2.54BCS | | |
| H1 | 6.40 | 6.68 | 7.00 |
| L | 12.48 | 12.98 | 13.48 |
| L1 | — | — | 0.85 |
| φP | 3.00 | 3.18 | 3.40 |
| Q | 3.05 | 3.30 | 3.55 |

TO-263-2L

UNIT: mm

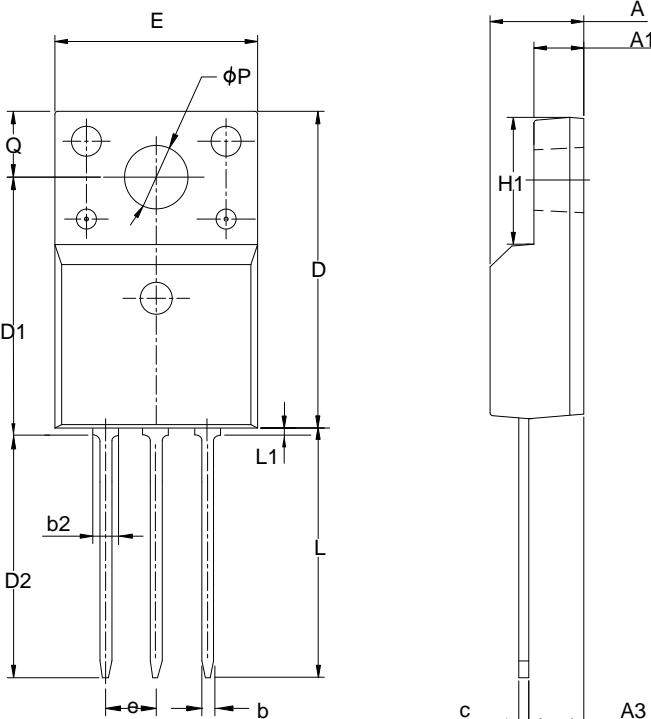


| SYMBOL | MILLIMETER | | |
|--------|------------|------|-------|
| | MIN | NOM | MAX |
| A | 4.30 | 4.57 | 4.72 |
| A1 | 0 | 0.10 | 0.25 |
| b | 0.71 | 0.81 | 0.91 |
| b1 | 1.17 | — | 1.50 |
| c | 0.30 | — | 0.60 |
| c2 | 1.17 | 1.27 | 1.37 |
| D | 8.50 | — | 9.35 |
| E | 9.80 | — | 10.45 |
| e | 2.54BSC | | |
| H | 14.70 | — | 15.75 |
| L | 2.00 | 2.30 | 2.74 |
| L1 | 1.12 | 1.27 | 1.42 |
| L2 | — | — | 1.75 |



PACKAGE OUTLINE

| TO-220FJHD-3L | | UNIT: mm | | |
|---------------|--|------------|-------|-------|
| SYMBOL | | MILLIMETER | | |
| | | MIN | NOM | MAX |
| A | | 4.42 | 4.70 | 5.02 |
| A1 | | 2.30 | 2.54 | 2.80 |
| A3 | | 2.50 | 2.76 | 3.10 |
| b | | 0.55 | 0.70 | 0.80 |
| b2 | | — | — | 1.29 |
| c | | 0.35 | 0.50 | 0.65 |
| D | | 15.25 | 15.87 | 16.25 |
| D1 | | 12.87 | 13.07 | 13.27 |
| D2 | | 12.28 | 12.48 | 12.68 |
| E | | 9.73 | 10.16 | 10.36 |
| e | | 2.54BCS | | |
| H1 | | 6.40 | 6.68 | 7.00 |
| L | | 12.48 | 12.98 | 13.48 |
| L1 | | — | — | 0.85 |
| φP | | 3.00 | 3.18 | 3.40 |
| Q | | 3.05 | 3.30 | 3.55 |



MOS DEVICES OPERATE NOTES:

Electrostatic charges may exist in many things. Please take following preventive measures to prevent effectively the MOS electric circuit as a result of the damage which is caused by discharge:

- The operator must put on wrist strap which should be earthed to against electrostatic.
- Equipment cases should be earthed.
- All tools used during assembly, including soldering tools and solder baths, must be earthed.
- MOS devices should be packed in antistatic/conductive containers for transportation.



Important notice :

1. Silan reserves the right to make changes of this instruction without notice.
2. Customers should obtain the latest relevant information when purchasing and should verify whether such information is latest and complete. Please read this instruction and application manual and related materials carefully before using products, including the circuit operation precautions, etc.
3. It is neither tested nor verified in accordance with AEC-Q series standards testing or application requirements. Silan does not give any warranties as to the suitability of the Silan's product for any specific use. The design intent, design definition and design of the product are not intended for application (the application stated in this instruction includes use, etc.) in transportation equipment, medical equipment, life-saving equipment, aerospace equipment, non-civil equipment or non-civil use, etc. (the equipment stated in this instruction includes systems, devices, etc., all referred to as equipment).The product should not be used in any equipment or system whose manufacture, use or sale is prohibited under any applicable laws or regulations("unintended use"). If the product is used for unintended use, therefore the full risks of such products application are borne by the customer and Silan assumes no liability for the product used for the unintended use. If the customer intends to use the Silan's product in a application where malfunction or failure can be reasonably be expected to result in personal injury, or serious property, or environment damage, the customer shall make adequate assessment, testing and verification, and Silan shall not be liable for such applications.
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SVSP65R380FJD(D)(FJH)(S)(FJHD)D4_Datasheet

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Rev.: 1.3

Revision History:

1. Add TO-220FJHD-3L package

Rev.: 1.2

Revision History:

1. Add TO-263-2L package
2. Update the curve

Rev.: 1.1

Revision History:

1. Modify ordering information

Rev.: 1.0

Revision History:

1. First release